

3 I/O OPTION A WITH CHECK SYNCHRONISATION

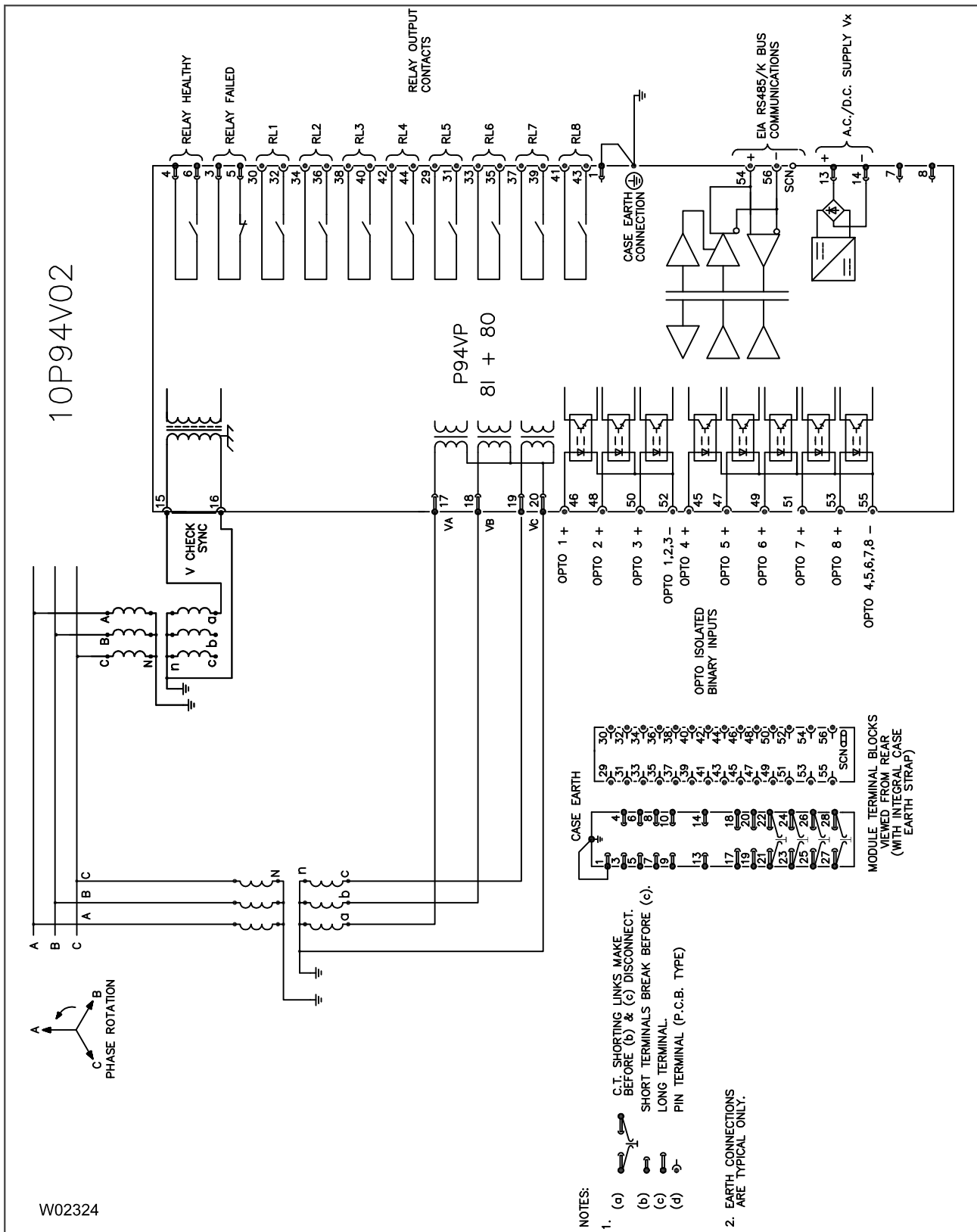


Figure 79: P94V Voltage and Frequency IED with Check Synchronisation input (Model P)